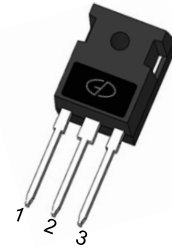


19mΩ,300V (D-S) Super Junction Power MOSFET

Features

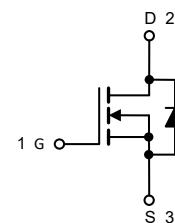
- 100% Avalanche Tested
- Extremely Low Losses with Low FOM $R_{ds(on)} \cdot Q_g$
- Halogen Free, Pb-Free
- RoHS Compliant



TO-247AD

Applications

- Solar inverter
- Telecom/Sever
- AC/DC power supply



Absolute Maximum Ratings ($T_J=25^\circ\text{C}$ unless otherwise noted)			
Parameter	Symbol	Value	Unit
Drain Source Voltage	V_{DS}	300	V
Gate Source Voltage	V_{GS}	± 20	V
Drain Current, Continuous $V_{GS}=10\text{V}$	I_D	$T_C=25^\circ\text{C}$	127
		$T_C=125^\circ\text{C}$	56
Drain Current, Pulsed (Note 1)	I_{DM}	380	A
Single Avalanche Energy (Note 2)	E_{AS}	2000	mJ
Power Dissipation	P_D	500	W
Operating Junction/ Storage Temperature Range	T_J/ T_{STG}	-55 to +150	$^\circ\text{C}$

Note 1: Single pulse; $t_p \leq 1\mu\text{s}$.

Note 2: $V_{DD} = 50\text{V}$, $I_D = 20\text{A}$, starting $T_J = 25^\circ\text{C}$.

Thermal Characteristics			
Parameter	Symbol	Max	Unit
Thermal Resistance Junction to Case	R_{thJC}	0.25	$^\circ\text{C/W}$
Thermal Resistance Junction to Ambient (Note 3)	R_{thJA}	62	$^\circ\text{C/W}$

Note 3: Device mounted on 1 square inch FR4 PCB board, with 2oz single-sided copper, in a 25°C still air environment.

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=1mA$	300	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=300V, V_{GS}=0V$	--	--	10	μA
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_{DS}=250\mu\text{A}$	2.5	--	4.5	V
Gate Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	--	--	± 100	nA
Drain-Source On-state Resistance (Note 4)	$R_{DS(on)}$	$V_{GS}=10V, I_D=36A$	--	13	19	$\text{m}\Omega$
Total Gate Charge	Q_g	$V_{GS(off)}=0V, V_{GS(on)}=10V, V_{DD}=200V, I_D=40A$	--	84	--	nC
Gate-Source Charge	Q_{gs}		--	27.5	--	
Gate-Drain Charge	Q_{gd}		--	21.7	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{GS}=10V, V_{DD}=200V, I_D=40A$	--	48	--	ns
Turn-on Rise Time	t_r		--	32	--	
Turn-off Delay Time	$t_{d(off)}$		--	83	--	
Turn-off Fall Time	t_f		--	8	--	
Gate Resistance	R_g	$V_{GS}=0V, f=1\text{MHz}, \text{open drain}$	--	3.5	--	Ω
Input Capacitance	C_{iss}	$V_{GS}=0V, V_{DS}=50V, f=1\text{MHz}$	--	5200	--	pF
Output Capacitance	C_{oss}		--	339	--	
Reverse Transfer Capacitance	C_{rss}		--	6.2	--	

Reverse Diode Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Forward Current, Continuous	I_{SD}	$T_C=25^\circ\text{C}$	--	--	127	A
Diode Forward Voltage (Note 4)	V_{SD}	$I_F=36A, V_{GS}=0V$	--	--	1.2	V
Reverse Recovery Time	T_{rr}	$V_R=200V, I_F=40A, di/dt=100\text{A}/\mu\text{s}$	--	118	--	ns
Reverse Recovery Charge	Q_{rr}		--	550	--	nC

Note 4: Pulse test; pulse width $\leq 380\mu\text{s}$, duty cycle $\leq 1\%$.

Typical Characteristics Curves (T_J = 25°C unless otherwise noted)

Fig.1 - Output Characteristics

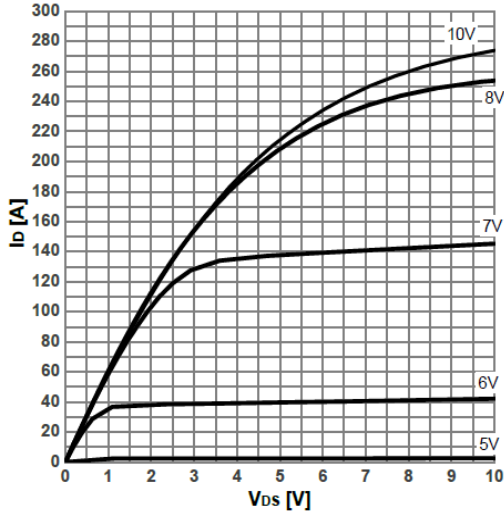


Fig.2 - Transfer Characteristics

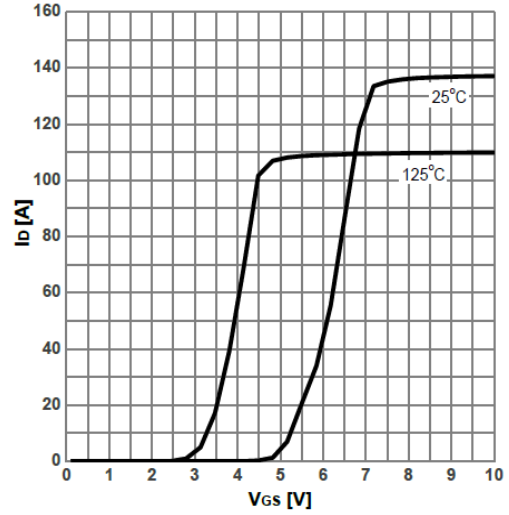


Fig.3 - Drain-Source On-Resistance

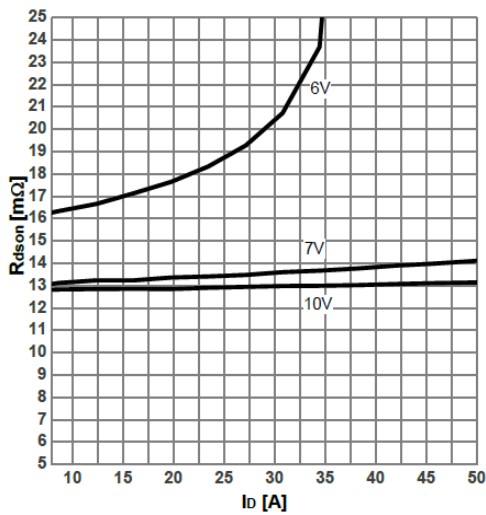


Fig.4 - Normalized On-Resistance

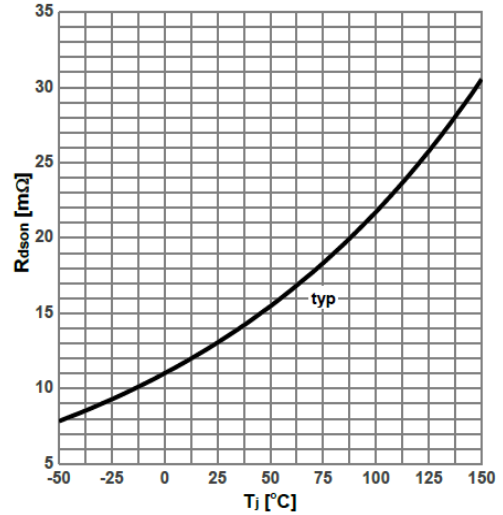


Fig.5 - Capacitance

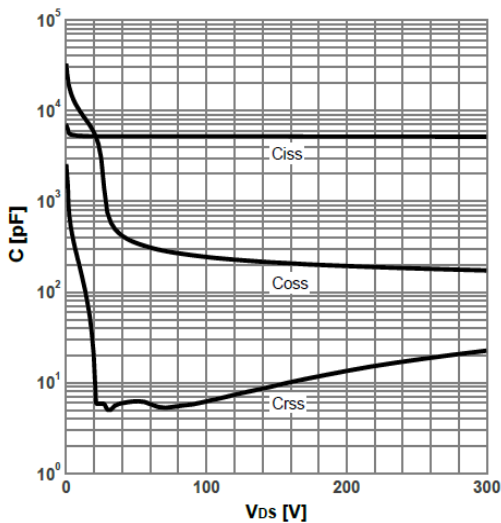
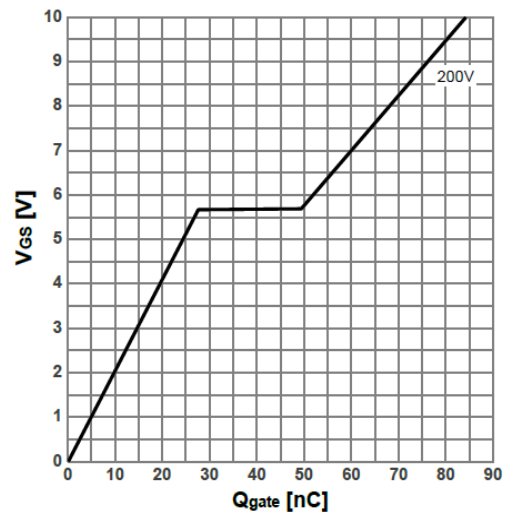


Fig.6 - Gate charge



Typical Characteristics Curves ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Fig.7 - Forward Characteristic

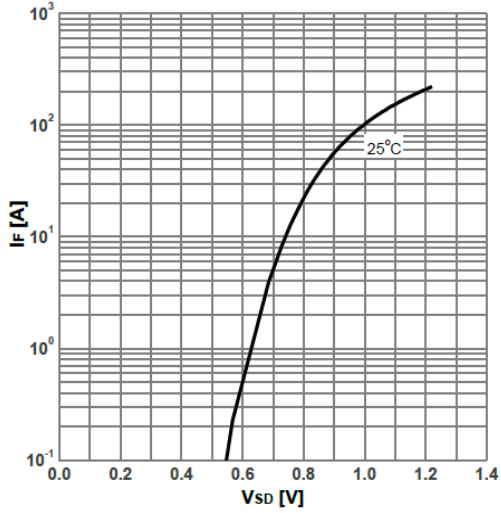


Fig.8 - Safe Operating Area

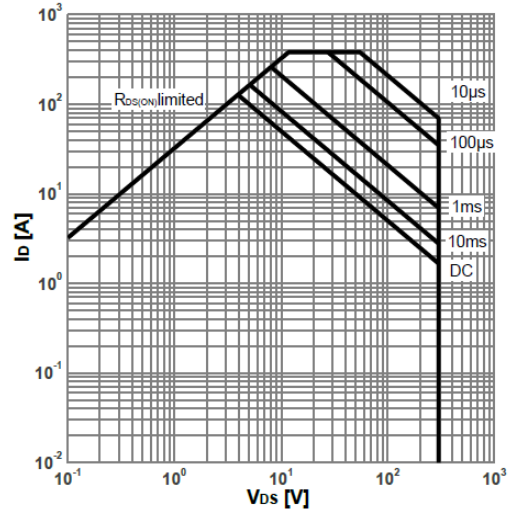


Fig.9 - Normalized Thermal Impedance, Junction-Case

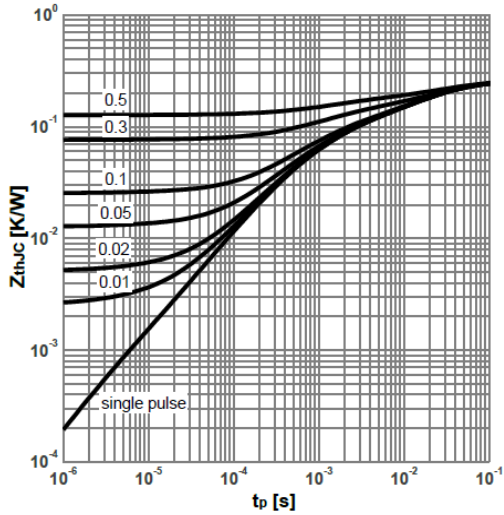


Fig.10 - Power Derating

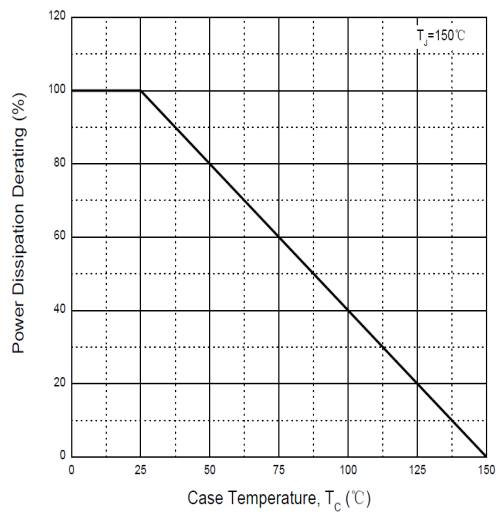
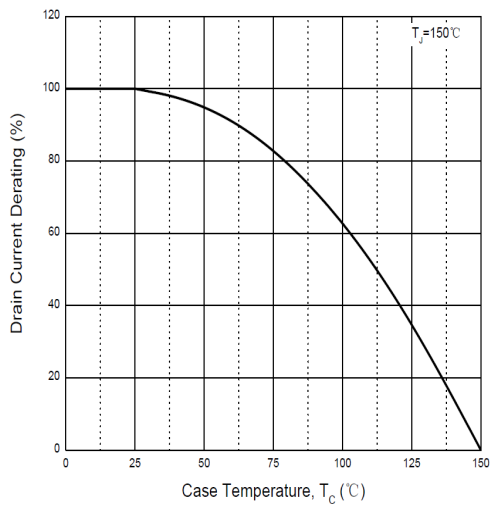
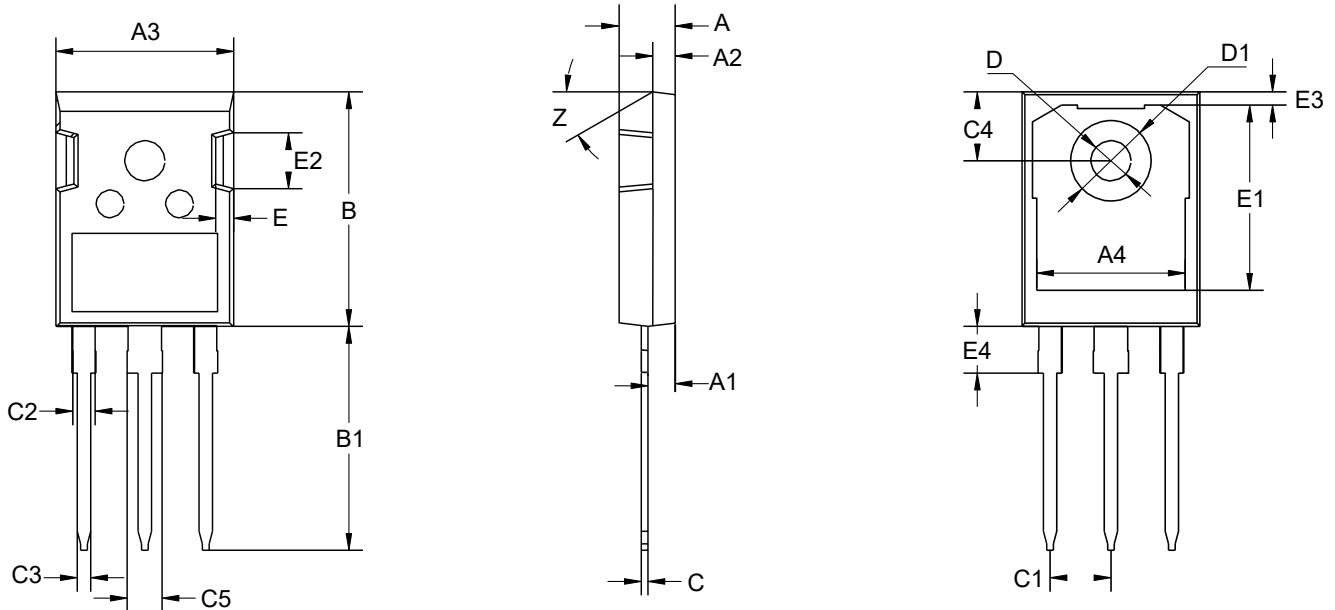


Fig.11 - Drain Current Derating



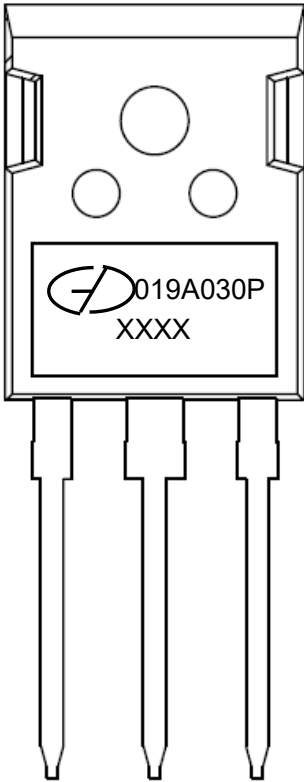
Package Outline Dimensions (Unit: millimeters)

TO-247AD




TO-247AD							
	Min.	Nom.	Max.		Min.	Nom.	Max.
A	4.7	5	5.2	C4	6.04	6.15	6.30
A1	2.3	2.4	2.5	C5	2.8	3	3.2
A2	1.9	2	2.1	D	3.5	3.6	3.7
A3	15.48	15.88	16.28	D1	7	7.19	7.4
A4	13.2	13.5	13.8	E	1.5	1.6	1.7
B	20.8	20.95	21.1	E1	16.25	16.55	16.85
B1	19.8	20	20.32	E2	4.9	5.0	5.1
C	0.5	0.6	0.7	E3	0.95	1.17	1.35
C1	5.34	5.44	5.54	E4	-	4.17	4.5
C2	1.8	2	2.2	Z	-	30°	-
C3	1.1	1.2	1.3		-	-	-

Marking Outline



Part Name: GMN019A030P

1. Logo Mark: 
2. P/N Mark: 019A030P
3. Date Code: XXXX

Revision History

Version	Date	Major Changes
Rev.A	2024.10.21	Official Release

Disclaimers

These materials are intended as a reference to assist our customers in the selection of the Suzhou Good-Ark product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Suzhou Good-Ark Electronics Co., Ltd. or a third party.

Suzhou Good-Ark Electronics Co., Ltd. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.

All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Suzhou Good-Ark Electronics Co., Ltd. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Suzhou Good-Ark Electronics Co., Ltd. or an authorized Suzhou Good-Ark Electronics Co., Ltd. for the latest product information before purchasing a product listed herein. The information described here may contain technical inaccuracies or typographical errors. Suzhou Good-Ark Electronics Co., Ltd. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors. Please also pay attention to information published by Suzhou Good-Ark Electronics Co., Ltd. by various means, including our website home page.

(<http://www.goodark.com>)

When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Suzhou Good-Ark Electronics Co., Ltd. assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.

The prior written approval of Suzhou Good-Ark Electronics Co., Ltd. is necessary to reprint or reproduce in whole or in part these materials.

Please contact Suzhou Good-Ark Electronics Co., Ltd. or an authorized distributor for further details on these materials or the products contained herein.