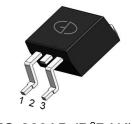




N-Channel 85V (D-S) Power MOSFET

Features

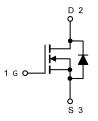
- 100% Avalanche Tested
- Extremely Low Losses with Low FOM Rdson*Qg
- Halogen Free, Pb-Free
- RoHS Compliant



TO-263AB (D²PAK)

Applications

- DC/DC
- Motors, lamps
- Power switching



Absolute Maximum Ratings (T _J =25°C unless otherwise noted)						
Parameter	Symbol	Symbol Value				
Drain Source Voltage	V _{DS}	85	V			
Gate Source Voltage	V_{GS}	±20	V			
Drain Current, Continuous V _{GS} =10V (Note 1) T _C =25°C		I _D	I _D 120			
Drain Current, Pulsed (Note 2)	I _{DM}	480	Α			
Single Avalanche Energy	Eas	440	mJ			
Power Dissipation (Note 3) T _C =25°C		P_{D}	220	W		
Operating Junction/ Storage Tempera	T _J / T _{STG}	-55 to +150	°C			

Note 1: Calculated continuous current based on maximum allowable junction temperature.

Note 2: Repetitive rating; pulse width limited by max. junction temperature.

Thermal Characteristics							
Parameter	Symbol	Max	Unit				
Junction-to-case (Note 3)	R _{eJC}	0.7	°C/W				
Thermal Resistance Junction-Ambient (Note 4)	R _{0JA}	58	°C/W				

Note 3: The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance. Note 4: The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25 \mathcal{C} .



Electrical Characteristics (T _J =25°C unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Drain Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	85			V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =80V, V _{GS} =0V			1	uA
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _{DS} =250uA	2		4	V
Gate Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V			±100	nA
Drain-Source On-state Resistance	R _{DS(on)}	V _{GS} =10V, I _D =20A		4.2	5.2	mΩ
Total Gate Charge	Qg			61		
Gate Source Charge	Q _{gs}	V _{GS} =10V, V _{DS} =40V, I _D =50A		21		nC
Gate Drain Charge	Q_{gd}			11		
Turn-on Delay Time	t _{d(on)}			21.6		
Turn-on Rise Time	t _r	V_{GS} =10V, V_{DS} =40V,		31.4		
Turn-off Delay Time	t _{d(off)}	I_D =40A, R_{GEN} =3 Ω		44		ns
Turn-off Fall Time	t _f			18		
Input Capacitance	C _{iss}			4645		
Output Capacitance	Coss	V _{GS=} 0V, V _{DS} =40V, f=1MHz		673		pF
Reverse Transfer Capacitance	C _{rss}			41		

Reverse Diode Characteristics (T _J =25°C unless otherwise noted)						
Parameter	Symbol	Symbol Test Conditions		Тур	Max	Unit
Continuous Source Current (Body Diode)	Is	T _C =25°C			120	А
Reverse Recovery Time	T _{rr}	I _S =20A,		69		ns
Reverse Recovery Charge	Q _{rr}	di/dt = 100 A/µs		141		nC





Typical Characteristics Curves (T_J = 25°C unless otherwise noted)

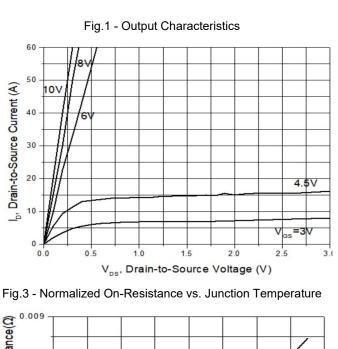
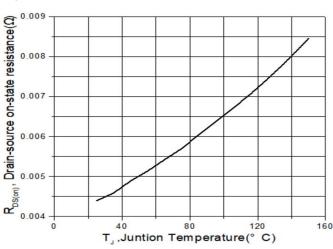
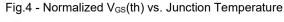


Fig.2 - Drain-to-Source Breakdown Voltage vs. Junction Temperature V_{BRDSS}, Drain-to-Source Breakdown Voltage (V) 101 100 98 97 96 T, Juntion Temperature (° C) 160





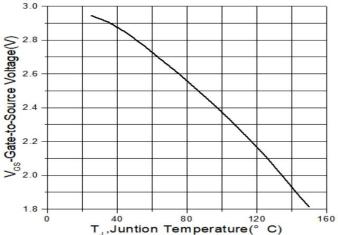


Fig.5 - Gate Charge Characteristics

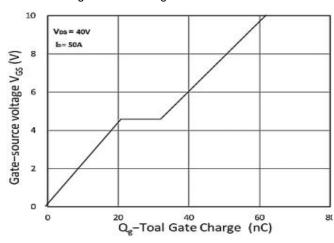
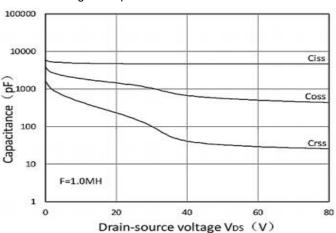


Fig.6 - Capacitance Characteristics





Typical Characteristics Curves (T_J = 25°C unless otherwise noted)

Fig.7 - Transfer Characteristics

1000 Drain current lo (A) 100µs Limited 10ms by R_{DS(en)} Single Pulse 0.1 T_c=25℃ T,=150°C

Fig.8 - Safe Operating Area

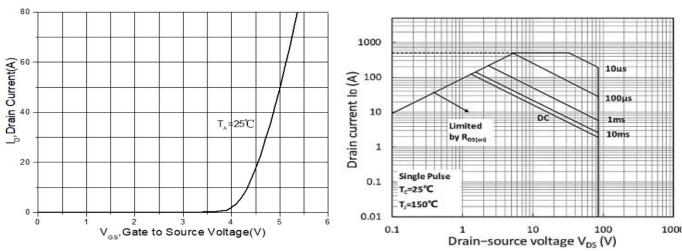
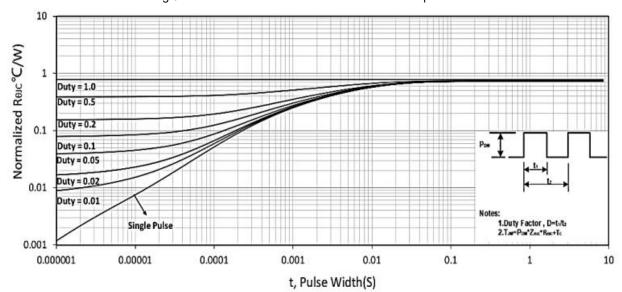


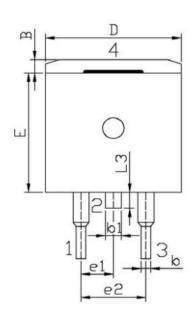
Fig.9 - Normalized Maximum Transient Thermal Impedance

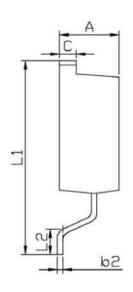




Package Outline Dimensions (Unit: millimeters)

TO-263

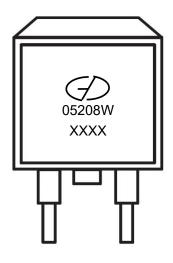




Symbol	Dimensions In	Millimeters	Symbol	Dimensions In Millimeters		
	Min	Max	Symbol	Min	Max	
A	4.30	4.70	Е	9.00	9.40	
В	1.00	1.40	e1	2. 34	2.74	
b	0.70	0.90	e2	4. 88	5. 28	
b1	1.15	1.35	L1	15.00	16.00	
b2	0.40	0.60	L2	2. 24	2.84	
C	1.20	1.40	L3	1.20	1.60	
D	9.80	10.20				



Marking Outline



Part Name: GMN05208W

1. Logo Mark:



2. P/N Mark: 05208W

3. Date Code: XXXX



GOOD-ARK Electronics

Disclaimers

These materials are intended as a reference to assist our customers in the selection of the Suzhou Good-Ark product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Suzhou Good-Ark Electronics Co., Ltd.or a third party.

Suzhou Good-Ark Electronics Co., Ltd. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.

All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Suzhou Good-Ark Electronics Co., Ltd. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Suzhou Good-Ark Electronics Co., Ltd. or an authorized Suzhou Good-Ark Electronics Co., Ltd. for the latest product information before purchasing a product listed herein. The information described here may contain technical inaccuracies or typographical errors. Suzhou Good-Ark Electronics Co., Ltd. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors. Please also pay attention to information published by Suzhou Good-Ark Electronics Co., Ltd. by various means, including our website home page. (http://www.goodark.com)

When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Suzhou Good-Ark Electronics Co., Ltd. assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.

The prior written approval of Suzhou Good-Ark Electronics Co., Ltd. is necessary to reprint or reproduce in whole or in part these materials.

Please contact Suzhou Good-Ark Electronics Co., Ltd. or an authorized distributor for further details on these materials or the products contained herein.