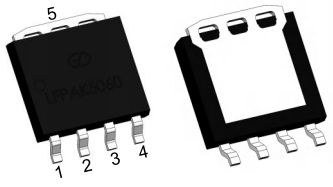
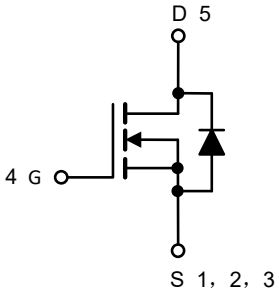
N-Channel 40V (D-S) Power MOSFET

Features

- 100% Avalanche Tested
- Extremely Low Losses with Low FOM Rdson*Qg
- RoHS Compliant, Halogen Free, Pb-Free
- AEC-Q101 Qualified
- MSL 1



LFPAK5060



Applications

- Automotive systems
- Motors, lamps and solenoid control
- Ultra high performance power switching

Absolute Maximum Ratings (T_J=25°C unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain Source Voltage		V_{DS}	40	V
Gate Source Voltage		V_{GS}	±20	V
Drain Current, Continuous V_{GS}=10V	T_C=25°C	I_D	199	A
	T_C=100°C		141	
Drain Current, Pulsed *(Note 1)*		I_{DM}	796	A
Single Avalanche Energy *(Note 2)*		E_{AS}	387	mJ
Power Dissipation	T_C=25°C	P_D	100	W
	T_C=100°C		50	
Operating Junction/ Storage Temperature Range		T_J/ T_{STG}	-55 to +175	°C

Note 1: Single pulse; t_p ≤ 1us.
Note 2: V_{DD} = 20V, V_{GS} = 10V, L = 0.5mH, R_G = 25Ω, starting T_J = 25 °C.

Thermal Characteristics

Parameter	Symbol	Max	Unit
Thermal Resistance Junction to Case	R_{thJC}	1.5	°C/W
Thermal Resistance Junction to Ambient *(Note 3)*	R_{thJA}	62.5	°C/W

Note 3: Device mounted on 1 square inch FR4 PCB board, with 2oz single-sided copper, in a 25°C still air environment.

Electrical Characteristics (T_J =25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS}=0V, I_D=250μA,	40	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=40V, V_{GS}=0V	--	--	1	uA
Gate Threshold Voltage	$V_{GS(TH)}$	V_{DS}=V_{GS}, I_{DS}=250uA	2	2.8	3.6	V
Gate Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS}=0V	--	--	±100	nA
Drain-Source On-state Resistance *(Note 4)*	$R_{DS(on)}$	V_{GS}=10V, I_D=20A	--	1.1	1.4	mΩ
Total Gate Charge	Q_g	V_{GS}=10V, V_{DS}=20V, I_D=20A	--	67	--	nC
Gate-Source Charge	Q_{gs}		--	21	--	
Gate-Drain Charge	Q_{gd}		--	16	--	
Turn-on Delay Time	$t_{d(on)}$	V_{GS}=10V, V_{DS}=20V, R_L=0.75Ω, R_G=3Ω	--	22	--	ns
Turn-on Rise Time	t_r		--	32	--	
Turn-off Delay Time	$t_{d(off)}$		--	49	--	
Turn-off Fall Time	t_f		--	22	--	
Gate Resistance	R_g	V_{GS}=0V, f=1MHz, open drain	--	3.3	--	Ω
Input Capacitance	C_{iss}	V_{GS}=0V, V_{DS}=25V, f=1MHz	--	4730	--	pF
Output Capacitance	C_{oss}		--	1970	--	
Reverse Transfer Capacitance	C_{rss}		--	72	--	

Reverse Diode Characteristics (T_J =25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Forward Current, Continuous	I_{SD}	T_C=25°C	--	--	99	A
Diode Forward Voltage *(Note 4)*	V_{SD}	I_F=25A, V_{GS}=0V	--	--	1.2	V
Reverse Recovery Time	T_{rr}	V_R =20V, I_F =20A, di/dt = 100 A/μs	--	74	--	ns
Reverse Recovery Charge	Q_{rr}		--	107	--	nC

Note 4: Pulse test; pulse width ≤ 380μs, duty cycle ≤ 1%.

Typical Characteristics Curves (T_J = 25℃ unless otherwise noted)

Fig. 1 - Output Characteristics

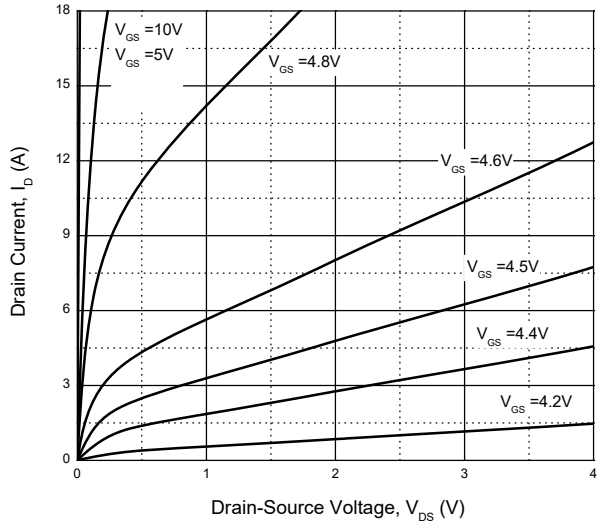


Fig. 2 - Transfer Characteristics

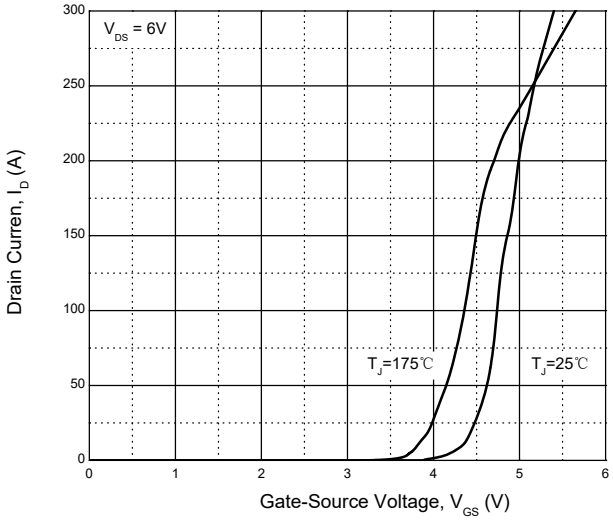


Fig. 3 - Drain-Source On-Resistance

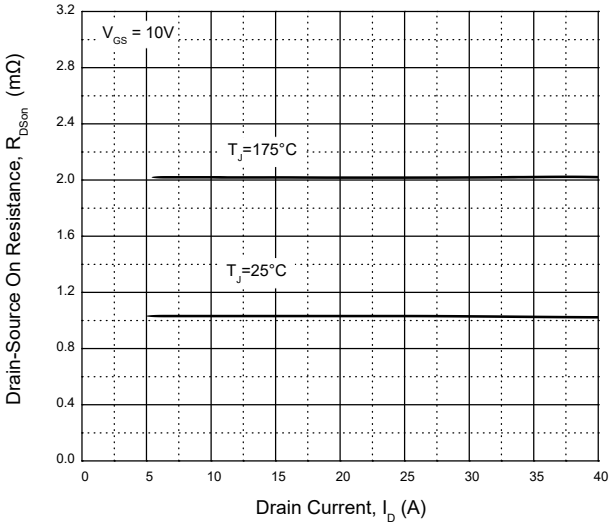


Fig. 4 - Normalized On-Resistance

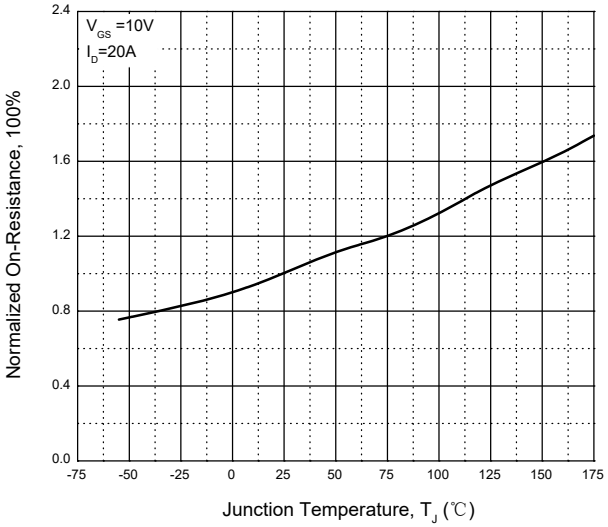


Fig. 5 - Drain-Source On-Resistance

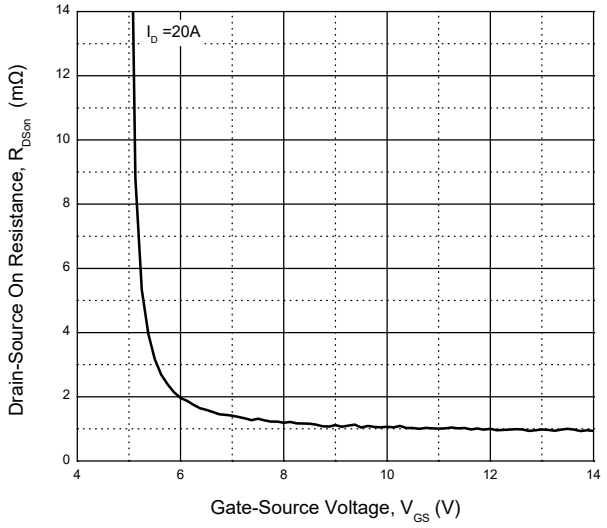
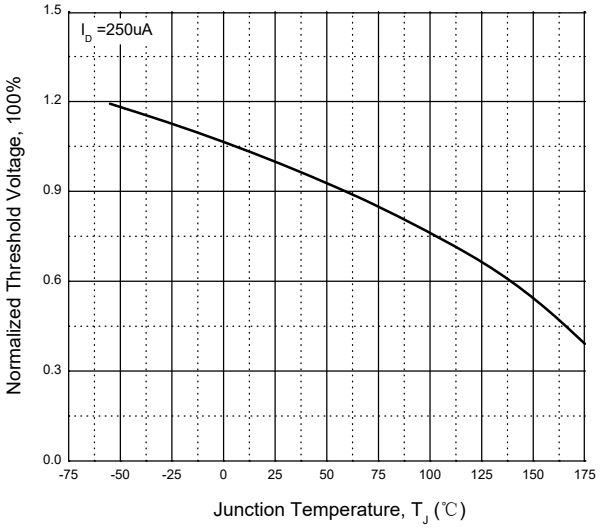


Fig. 6 - Normalized Threshold Voltage



Typical Characteristics Curves (T_J = 25℃ unless otherwise noted)

Fig. 7 - Capacitance

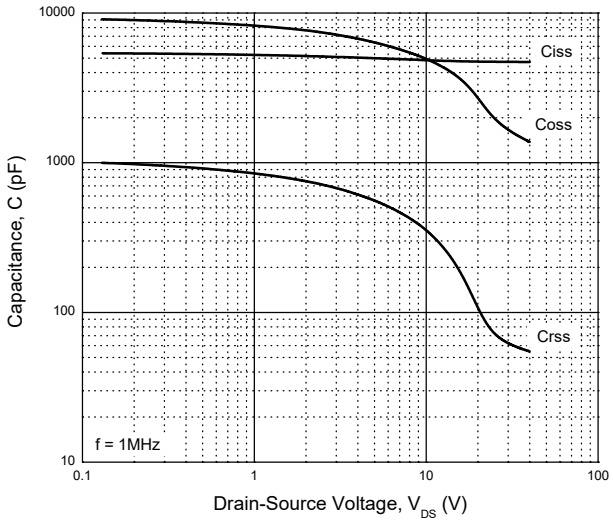


Fig. 8 - Gate Charge

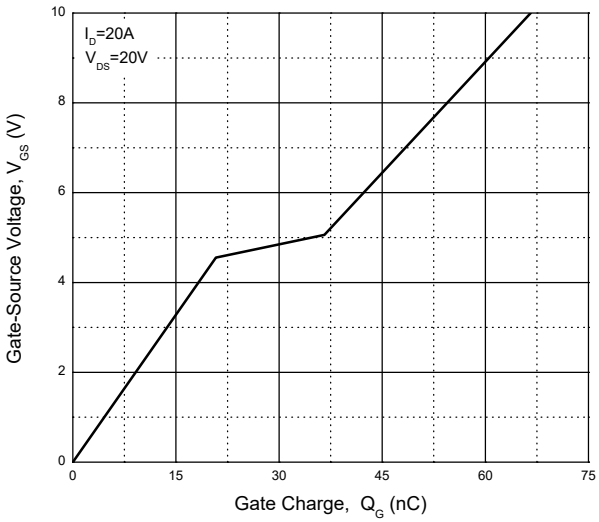


Fig. 9 - Forward Characteristic

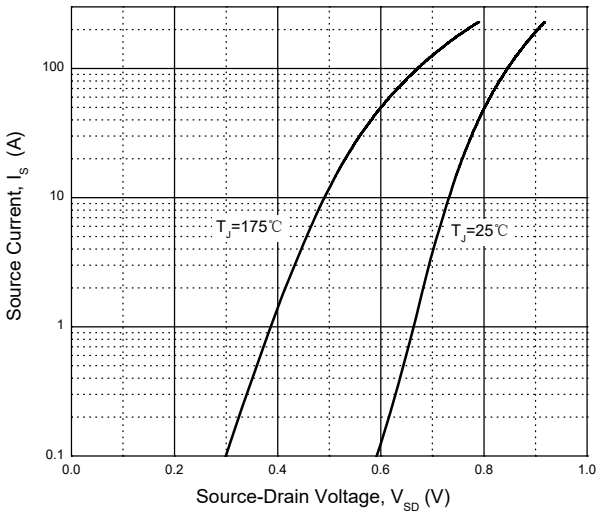


Fig. 10 - Safe Operating Area

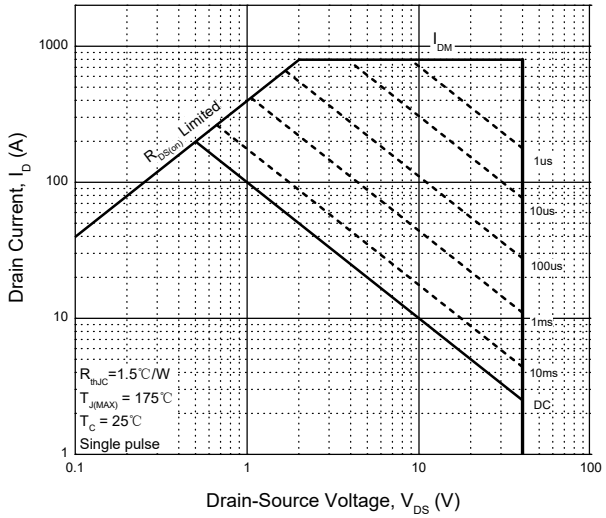
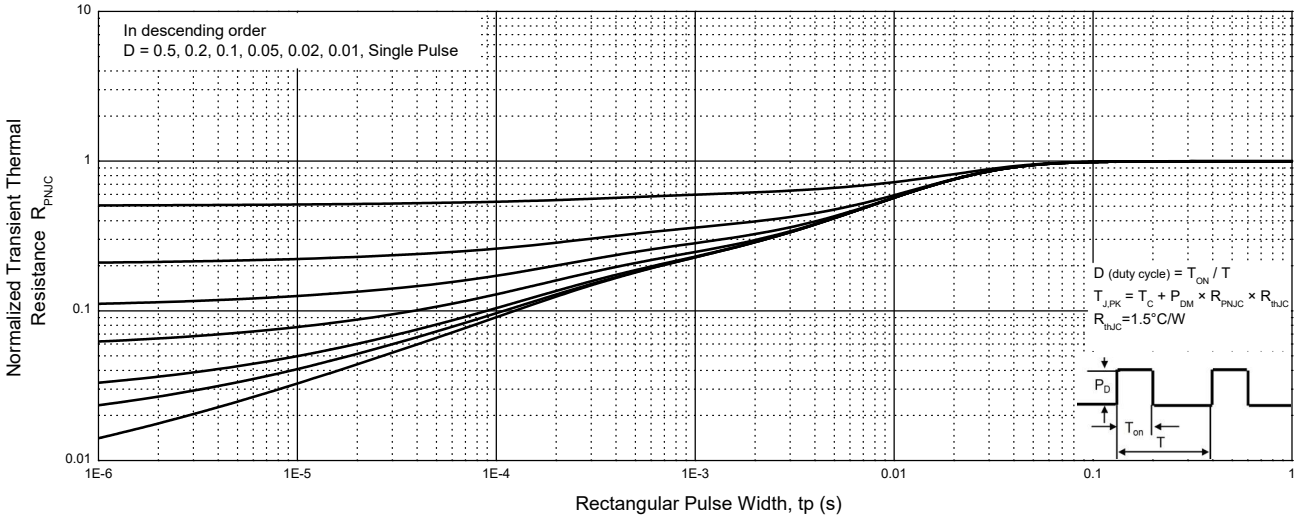


Fig.11 - Normalized Thermal Impedance, Junction-Case



Typical Characteristics Curves (TJ = 25°C unless otherwise noted)

Fig. 12 - Power Derating

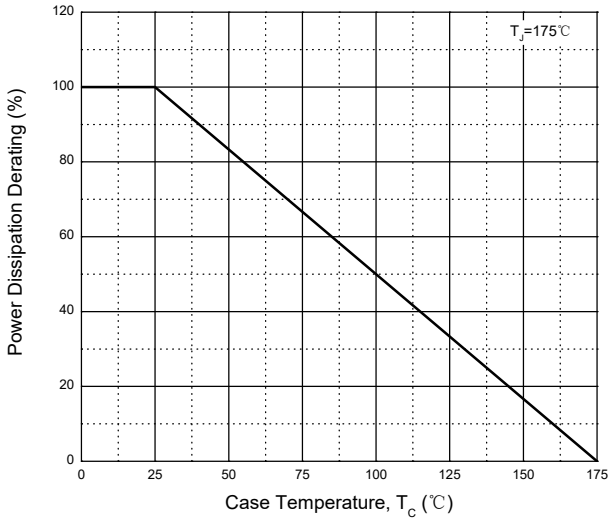
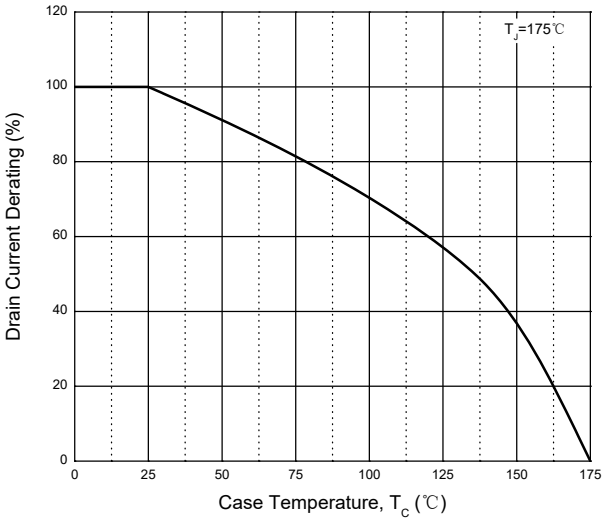
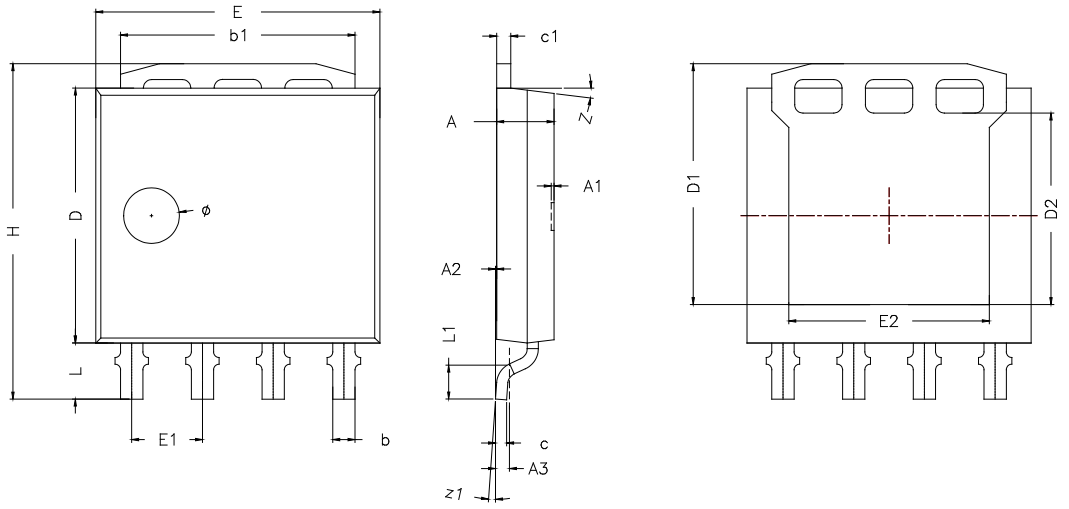


Fig. 13 - Drain Current Derating



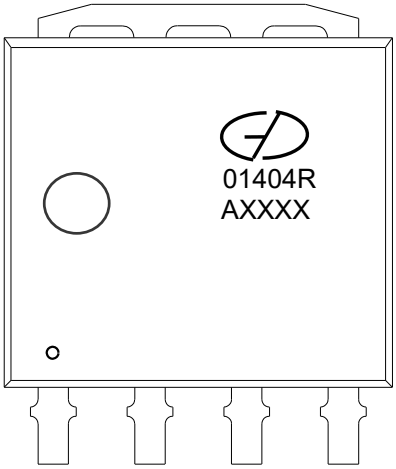
Package Outline Dimensions (Unit: millimeters)

LFPAK5060



LFPAK5060							
	Min.	Nom.	Max.		Min.	Nom.	Max.
A	0.980	1.030	1.080	A2	0	-	0.15
A1	-	0.050	-	A3	-	0.254	-
b	0.300	0.400	0.500	E	5.000	5.100	5.200
b1	4.110	4.210	4.310	E1	1.170	1.270	1.370
c	0.190	0.200	0.250	E2	3.450	3.600	3.750
c1	0.240	0.254	0.300	L	0.800	1.010	1.300
D	4.490	4.590	4.690	L1	0.300	0.510	0.750
D1	-	4.338	4.800	Z	-	7°	-
D2	-	3.450	-	H	5.940	6.040	6.240
Z1	0°	-	8°	-	-	-	-

Marking Outline



Part Name: AGMN01404R

1. Logo Mark:
2. P/N Mark: 01404R
3. Date Code: AXXXX
4. Pin 1#: o

Revision History

Version	Date	Major Changes
Rev.A	2026.03.31	Official Release

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